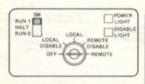
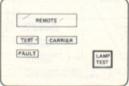
B. D. PIERCE

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PDP-11/70

ELECTRONIC CONSOLE COMMANDS





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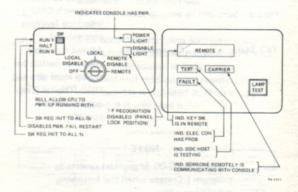
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PDP-11/70 ELECTRONIC CONSOLE COMMANDS



KEYSWITCH POSITION DESCRIPTION

OFF Turns CPU and electronic

console off.

LOCAL DISABLE ^P recognition disabled, disables

all console functions.

LOCAL ^P recognition enabled, enables

all console functions at local terminal.

rminai.

REMOTE DISABLE

AP recognition disabled, disables all console functions, forces local

copy.

REMOTE

^P recognition enabled, enables all console functions at remote

terminal.

THREE SEPARATE STATES

Program I/O State	Command Z	Allows communica-
Console State	Command ^P	Console communicates with CPU interface (accepts commands)
Talk State	Command AL	Allows two-way. communication be- tween local site and DDC (Digital Diag- nostic Center)
		operator

NOTE

Console (V02-00 or greater) reverts to program I/O state when the following conditions occur:

- 20 seconds have elapsed without using console, and
- Program is running, and
- · Character Ready bit is set, and
- Key switch in LOCAL.

LEGEND:

All RED symbols typed by human. All other symbols typed by CPU.

۸p	Means typing the control key and the letter
	P at the same time to perform the command.
\$	Means typing \$ to perform the command.
n sidenes d	Is used to indicate that a numeric variable is REQUIRED.
[n]	Is used to indicate that a numeric variable is OPTIONAL.
m	Is content of console switch register.
<nl></nl>	Means that a <cr> and <lf> were generated by the CPU.</lf></cr>

CONSOLE CONTROL COMMANDS

^E	The result to the trial libraries of O and
	Example: 11/70 V01-00.
^L	Sets talk state; ignores all characters except ^P.
AP	multiplexer to console physical; puts data
[n] ^R	display multiplexer to data paths. Will load and read control register; if data precedes *R it will load it; if no data precedes *R it will read it.

CONTROL REGISTE

	4		2	1	0
1 SUPPRESSES REMOTE TYPEOUT	SPECIAL MODE	EXPAND PRINTOUT	1 TALK ECHO	+ LOCAL CONTROL	1 LOCAL COPY
	SUPPRESSES SYNTAX ERRORS RUNNING ERRORS	COMMAND AFTER EACH	ENABLES LOCAL COPY IN TALK MODE	ENABLES LOCAL CONTROL IN REMOTE MODE	LOCAL COPY IN REMOTE MODE

^{*} THESE BITS CAN NOT BE SET FROM LOCAL TERMINA

TK-122

^U	Clears the data typed in the temporary data register.
\$	Sets a flag to indicate a register versus a memory location; Example: \$n/XXXXX
	will examine contents of general register n.
	Data separator before a start or go command; Example: 30,17765000G.
V	Verifies console logic; will print V000377 if no fault is detected.
7	Sate estial line multiplement and 110

Z Sets serial line multiplexer to program I/O state.

<RO> Rub out; deletes the rightmost octal charac-

ter from the temporary register, DEL KEY causes "rubout" action to occur. Example: 1234\4\3\2\1\0\0 (For V01-00 consoles) 1234\4321\ (For V02-00 consoles) *

^{*} On V02-00 the backslash only occurs on the first and last character being deleted from the buffer.

CPU CONTROL ODT-11 COMMANDS

n^D	Dumps successive memory locations contin- uously, until a character is typed, starting at address n.
[n] <lf></lf>	Deposits any data typed and opens the next sequential location.
[n] <cr></cr>	Deposits any data typed and closes the location to further modification.
" elsb is - els en	Changes to hex data format from octal; data is displayed in hex while address display does not change.*
	Changes to octal data format from hex.* Example: ^P
	CON = 0/123456" 00000000/"A72E' 00000000/123456
n/	Performs load address and examine; effectively opens word location n.
: [n]@	Reserved for future expansion. Deposits any data typed and uses contents of currently open location as address of
В	location to open next.
[m,] nG	Reserved for future expansion.
[III,] IIG	Go command begins at address n with switch register set to m; places console in program I/O. Example: 30,17765000G
P	Proceed command places console in program I/O. It continues program from address in program counter.
n\	Performs load address and examine; effectively opens byte location n. Example:*
	CON= 0/XXXXXX 123456 <cr> CON= 0\056 <lf> CON= 1\247 <cr></cr></lf></cr>

CON=

CONSOLE MESSAGES

"? SYN ER"	Syntax error; command is
	given out of sequence or
	with missing character.
"? RUN ER"	Illegal when CPU running;
	certain commands are il-
	legal with CPU running.
	Example: Start, load
The same	address, and initialize.
"? ER/TXXXXX"	Memory reference error;
	address/data parity error
	detected during a memory
	reference.
Fault Indicator	Console logic fault; con-
	sole logic has detected an
	error within itself.
"+"	Serial line error; identifies
	framing or overrun errors with a "+".
"#"	CPU response time out; #
	is printed if CPU did not
	respond to a console com-
	mand.
"*HXXXXXXXX/TXXXXX"	Programmed halt; console
	will print "halt notifica-
	tion" and HALT ADDR/
	status.
"? CAR ER"	Carrier lost.

^{*} Command is available on consoles with an ID of V02-00 or greater.

CPU CONTROL BASIC COMMANDS

[n] A Prints address display in octal as received from one of eight inputs to address multiplexer.

n = 0 Program Physical

n = 1 Kernel Data

n = 2 Kernel Inst.

n = 3 Console Physical

n = 4 Supervisor Data

n = 5 Supervisor Inst.

n = 6 User Data

n = 7 User Inst.

NOTE

AP or "Power On" set position 3; if octal # precedes A, new position is set.

C Continues program from address stored in program counter.

[n] D Deposit; sequential deposits are possible (location must be open).

Examine; sequential examines are possible.

Halt; address counter and CPU status will be printed.

Initialize CPU.

E

н

Set single bus cycle.

K Reset single bus cycle.

nL Load address n.
[n] M Prints data regist

Prints data registers: n = 0 Bus Register

n = 1 Data Paths (shifter)

n = 2 Display Register

n = 3 CPU HADRS

NOTE

Position 1 is set by ^P, E or "Power on." To change data path's position, type new position n first then type M.

N Execute next instruction/bus cycle.

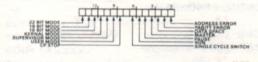
R Read switch register.

[m,] nS Start the CPU at the address n with switch

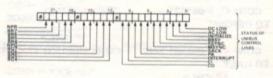
register set to m.

T Read CPU status.

STATUS RESPONSE BIT DEFINITION



U Print the state of the Unibus in octal.



-

V Write

Write n into switch register.

EXAMPLES

BASIC COMMANDS WITH ODT START PROGRAM AP <NL> AP <NL> CON= 200S <NL> CON= 200G CON= Z CONTINUE PROGRAM AP CON= C Z <NL> CON= P < NL> READ ROM STATE AP <NL> CON= 3MXXXXXXXX DISPLAY ADRS IN CON PHY AP <NL> CON= AXXXXXXXX TO EXAMINE AP <NL> AP <NL> CON= 4L E 051531 CON= 4/051531 TO DEPOSIT AP <NL> AP <NL> CON= 4L E 051531 77D CON= 4/051531 77 <CR> TO HALT AP <NL> CON= H 01731240/T14410 TO CK FOR RUNNING CPU

CON= T XX0XX CPU IS RUNNING

AP

An example of how to single step a program using the electronic console is shown below.

ΛP

CON= 1000/XXXXXXX 12737<LF> This program 00001002/XXXXXX 101<LF> will print the 00001004/XXXXXX 177566<LF> letter A 00001006/XXXXXX 0<CR> CON= 1000L | N00001006 A means program moved

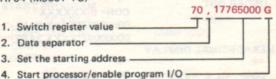
character to terminal print buffer not a human

WITH ODT HEXADECIMAL DEPOSIT ONLY POSSIBLE UNDER ODT CON= 0/XXXXXXX" 0000000/"XXXX 2AF1<LF> 0000002/"XXXX 2AF2<CR> HEXADECIMAL DISPLAY AP ΛP CON= "OL E"XXXX < CR> CON= 0/XXXXXXX" CON= E"XXXXX"<CR> 00000000/"XXXX<LF> CON= EXXXXXX 00000002/"XXXX<CR> HEXADECIMAL CONTINUOUS EXAMINES CON= " 0^D 00000000/"XXXX XXXX... OPEN BYTE LOCATION IN HEX ΛP CON= 0\XXX" 00000000\"XX<LF> 00000002\"XX OPEN BYTE LOCATION AND DEPOSIT CON= 0\XXX<LF> 00000001\XXX<LF> 00000002\XXX 24<LF> 00000003/XXX OPEN BYTE LOCATION IN A REGISTER AP CON= \$0\XXX<LF> 17777700H\XXX<LF> 17777701L\XXX

BASIC COMMANDS

TO BOOT THE SYSTEM

Bootstrap Example with initial switch value for an RP04 (M9301-YC)



M9301-YC BOOTSTRAP (Type ^P H before attempting to boot.)

10,17765000G	TM11	
20,17765000G	TC11	
30,17765000G	RK05	
40,17765000G	RP03/RP05/RP06	
50,17765000G	RK06/RP07	
60,17765000G	TU16	
70,17765000 G	RP04	
100,17765000G	RS04	
110,17765000G	RX01	

Bootstrap Example with an initial switch value for an RP06 (M9312)

Co	nditions	Using Basic Commands
1.	 Diagnostic ROM in E20 RP06 ROM in E35 socket (ROM 1) Unit 0 (Drive 0) 	56,17765744G
2.	Same as in #1 except for RP06 ROM in E34 socket (ROM 3)	456,17765744G
3.	Same as in #2 except for Unit 2 (Drive 2)	2456,17765744G
	UNIT 2	
	ROM 3	
	RP06 —	
	BOOT ADDRESS -	